

123.(New) The apparatus defined in claim 102 wherein the clock information of all of the CDR signals has a predetermined common frequency.

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cont

124.(New) The apparatus defined in claim 103 wherein the reference clock signal has frequency having a predetermined relationship to the predetermined common frequency.

125.(New) The apparatus defined in claim 104 wherein the first circuitry comprises phase locked loop circuitry.

126.(New) The apparatus defined in claim 104 wherein each of the second circuitries comprises:

selection circuitry that selects at least one of the candidate recovered clock signals that is close in phase to the clock information in the CDR signal received by that second circuitry.

127.(New) The apparatus defined in claim 104 wherein each of the second circuitries comprises:

selection circuitry that selects one of the candidate recovered clock signals that is closest in phase to the clock information in the CDR signal received by that second circuitry.
